

FIG. 1

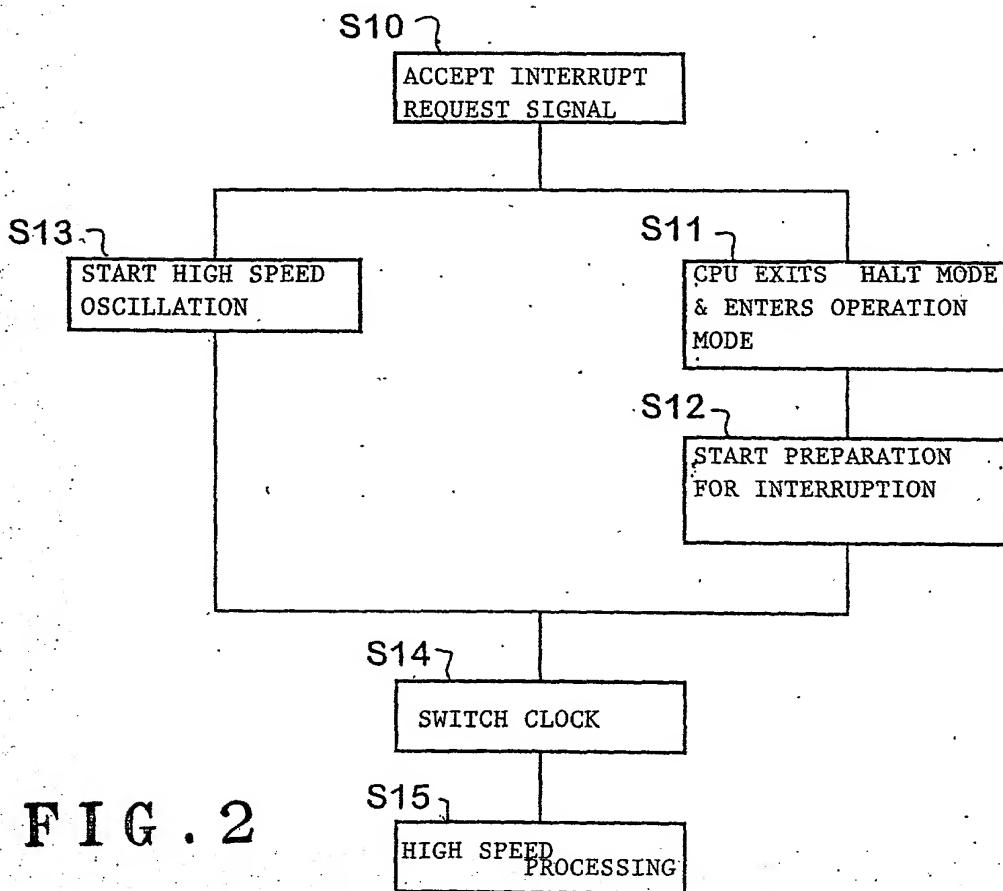


FIG. 2

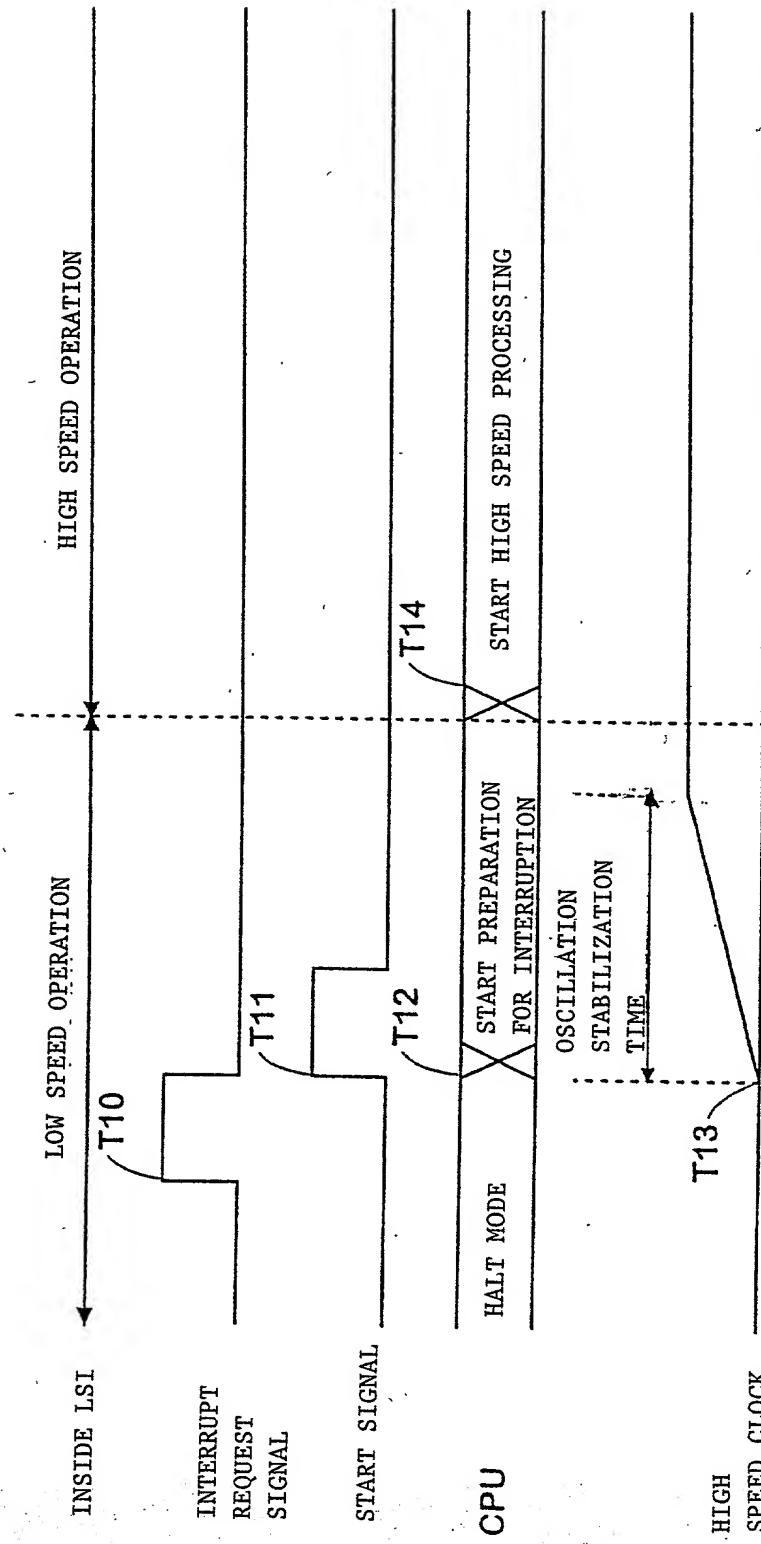


FIG. 3

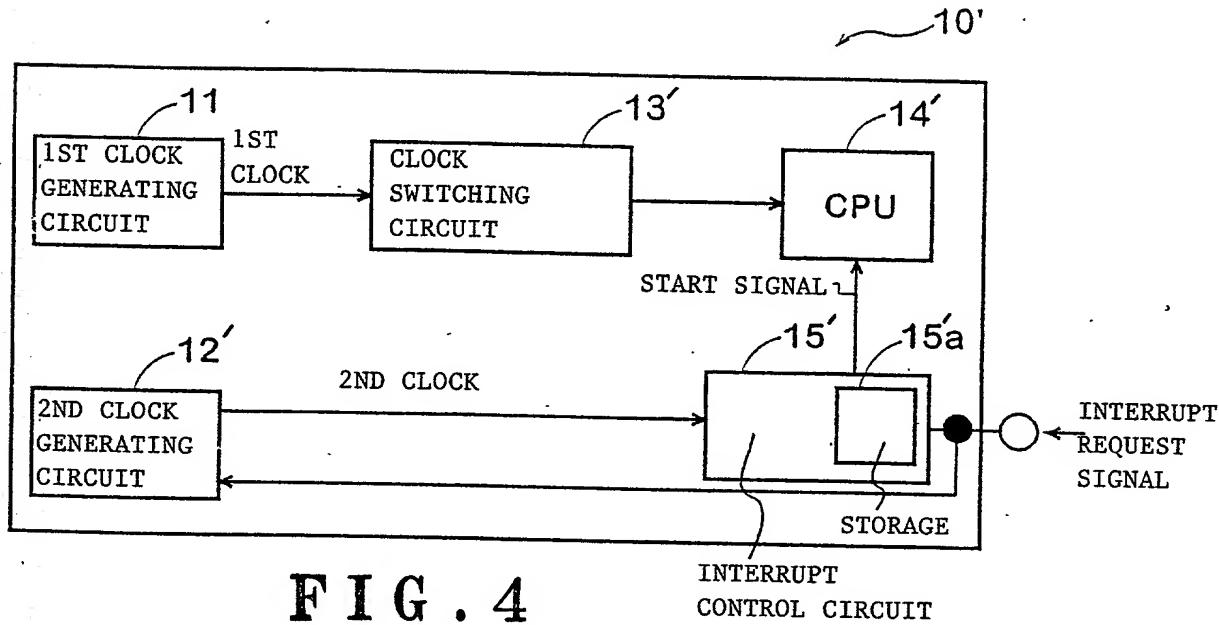


FIG. 4

INTERRUPT
CONTROL CIRCUIT

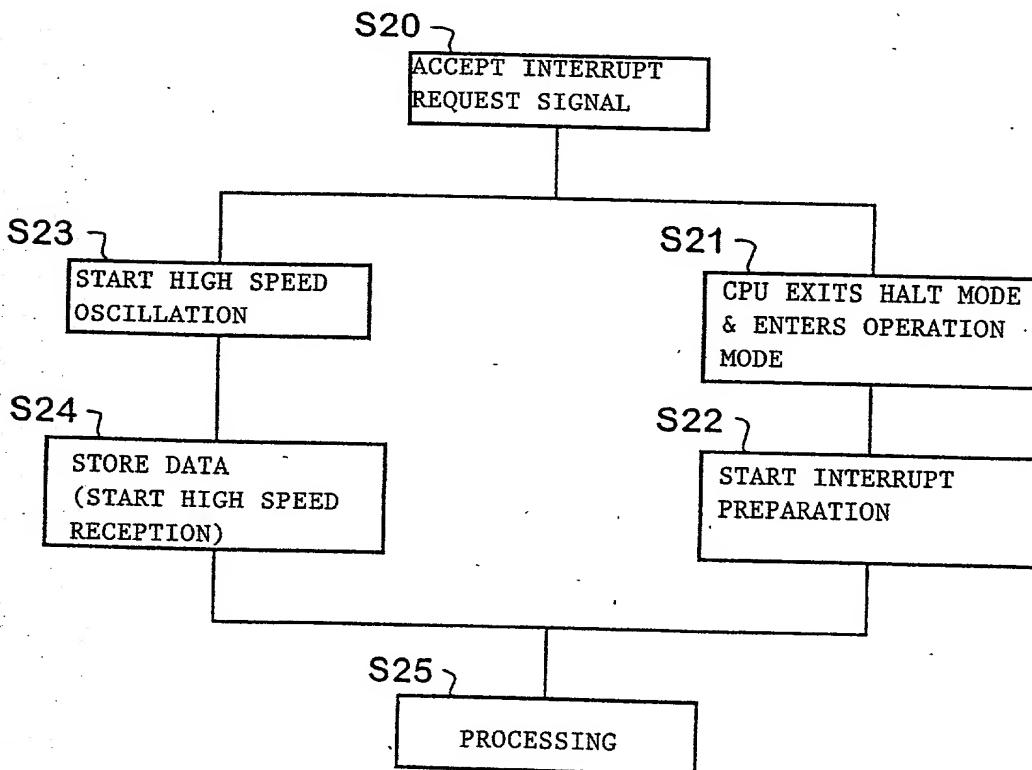


FIG. 5

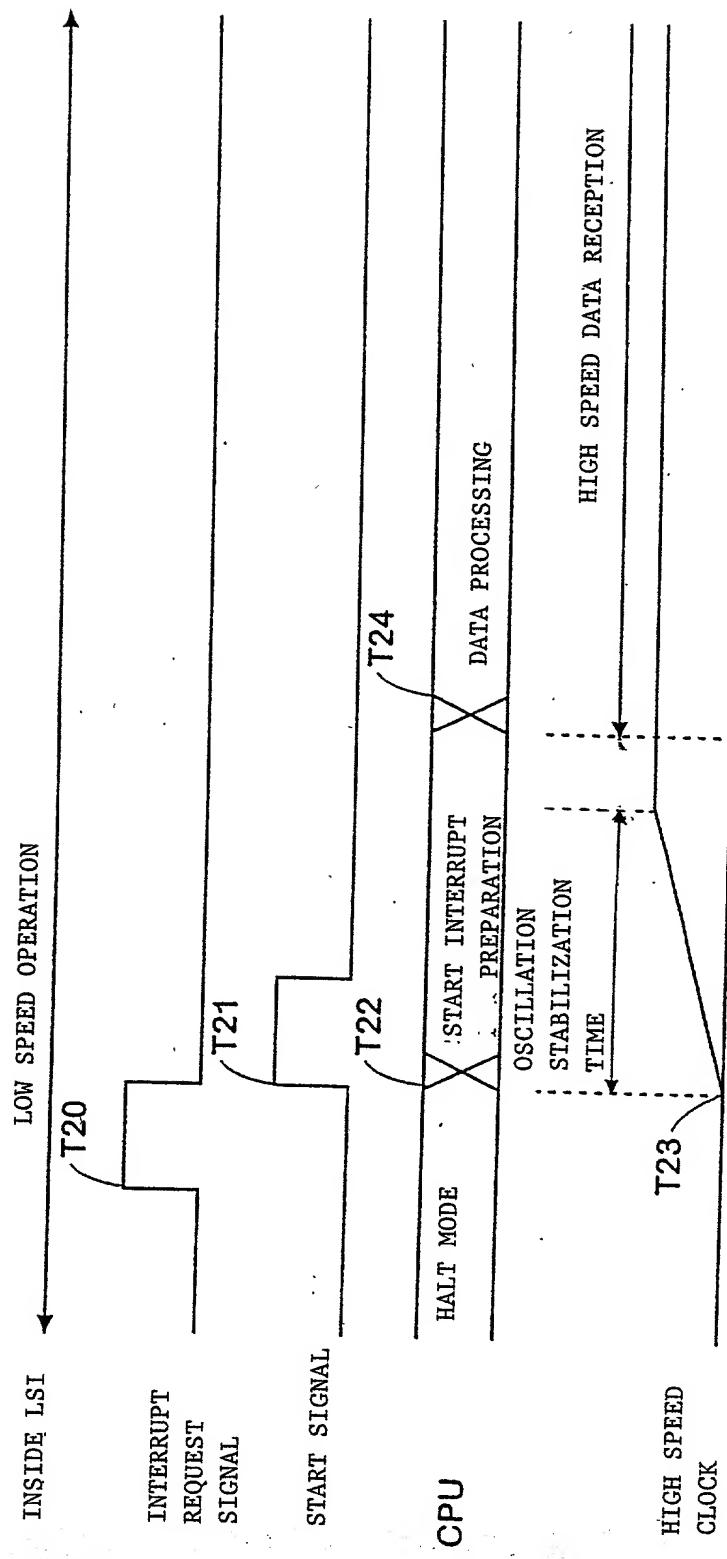


FIG. 6